

EXPANSION INTERFACE BUS + VIDEO OUT : TRS - VIDEO GENIE - PRF

TRS	VG	PROF	SIGNAL	PRF	VG	TRS	SIGNAL
3-7-11-15	1	8 29 37	GND		2	8 29 37	GND
20	3	36	A7	17	4	38	A6
14	5	35	A5	12	6	31	A4
16	7	27	A1	21	8	34	A3
18	9	40	A2	13	10	25	A0
5	11	28	D5	6	12	32	D2
	13		NC	4	14	22	D1
1	15	30	D0	8	16	26	D3
9	17	20	D7	2	18	24	D6
	19		VCC	10	20	18	D4
	21		A15		22		A8
	23		A14		24		A9
	25		NC		26		A10
	27		A13		28		A11
	29		A12		30		PHI
	31		PINT		32		NC
	33		NC		34		PHLDA
24	35		PHANTOM		36		HALT
	37	33	PWAIT		38 (12 ou 19)*		IORQ
	39		PHOLD		40		WR
	41		RD		42		CCDBS
	43		MREQ		44		DODBS
	45		M1	45	46	2	RESET
	47		RFSH		48		NMI
3-7-11-15	49	8 29 37	GND	3-7-11-15	50	8 29 37	GND
26		19	IN*				
29		12	OUT*				

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SIGNAL DESCRIPTION

DESCRIPTION SIGNAUX

- PHI 1.78 MHz Clock
- PINT Interrupt
- NC No Connection
- PHLDA Processor Hold Acknowledge
- PHANTOM Phantom
- HALT Halt Acknowledge
- PWAIT Processor Wait
- IORQ Input/Output Request
- PHOLD Processor Hold
- WR Processor Write
- RD Processor Read
- CCDBS Control and Status Bus Disable
- MREQ Memory Request
- DODBS Data and Address Bus Disable
- M1 First State of Instruction Cycle
- RESET CPU Reset
- RFSH Dynamic Memory Refresh
- NMI Non-maskable Interrupt

